

App. No. 10/754,416
Office Action Dated May 3, 2006

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Amendments to the Claims

The following claim listing is intended to replace previous claim listings for this application.

1-11. (canceled)

12. (currently amended) A method for manufacturing a double-sided circuit board, comprising:

providing a through hole in a laminate in which mold release films are formed on both surfaces of a circuit board electrically insulating material that is a circuit board electrically insulating sheet comprising a porous sheet in which a resin layer is laminated to at least one surface of the porous sheet and at least a central portion of the porous sheet resin layer is not completely impregnated with a resin in pores that are present inside the porous sheet, and only a central portion of the porous sheet is not impregnated with resin from the laminated resin layer and the circuit board electrically insulating material has a sufficiently small thickness for use in a circuit board;

filling the through hole with a conductive paste;

peeling off the mold release film from the laminate in which the through hole is filled with the conductive paste;

superimposing metal foils on both surfaces of the circuit board electrically insulating material from which the mold release films have been peeled off to form a laminate;

heating and pressing the laminate to allow hollow pores of the porous sheet to be filled with resin and allow the metal foils to be adhered to the porous sheet, and compressing and hardening the conductive paste filled in the through hole, thereby providing an inner via hole; and

forming desired circuit patterns on the metal foil.

13. (original) The method for manufacturing a double-sided circuit board according to claim 12, wherein the conductive paste contains a conductive particle and a resin as a main component and the average hole diameter of the pores of the porous sheet is smaller than the average particle size of the conductive particle.

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14. (original) The method for manufacturing a double-sided circuit board according to claim 13, wherein the conductive paste comprises conductive particles in the range from 70 to 95 weight % and resin in the range from 5 to 30 weight %.

15. (original) The method for manufacturing a double-sided circuit board according to claim 12, wherein the maximum hole diameter of the pores of the porous sheet is 10 μ m or less.

16. (original) The method for manufacturing a double-sided circuit board according to claim 12, wherein the porous sheet is a non-woven fabric containing a synthetic fiber as a main component.

17 - 46. (canceled)